

JAI NARAIN VYAS UNIVERSITY, JODHPUR
FACULTY OF ENGINEERING

NO.: JNVU/FE/P:Exam/CSE/2016/SP-3

DATE: 03rd September 2016

NOTICE

The Practical & Sessionals examination of the BE III year VI Semester examination 2016 (**Electronics and Computer Engineering**) will be held as per the programme given below. All concern to please note.

S. No.	Subject	Roll. No.			
		15ECC50001-15ECC50018	15ECC50019-15ECC50036	15ECC50037-15ECC50054	15ECC50055-Rest
1.	Digital Communication Lab	07/09(M)	07/09(A)	08/09(M)	08/09(A)
2.	Microprocessor Lab	09/09(M)	09/09(A)	10/09(M)	10/09(A)
3.	Computer Graphics Lab	10/09(M)	10/09(A)	09/09(M)	09/09(A)
4.	Business Automation Lab	13/09(M)	13/09(A)	14/09(M)	14/09(A)
5.	Power & Industrial Electronics Lab	14/09(M)	14/09(A)	13/09(M)	13/09(A)

(M): Morning session starts from 8.00 a.m. onwards

(A): Afternoon session starts from 11.00 a.m. onwards



(Dr. Anil Gupta)

Copy to following for information and necessary action please:

1. The Dean, Faculty of Engineering.
2. The Head of the Dept. **Electronics**
3. Examiner concerned.
4. The Asstt. Registrar, Secrecy/Examination/Engineering
5. P.S. to Vice Chancellor/Registrar, J.N.V. University, Jodhpur



(Dr. Anil Gupta)

Coordinator (Examination)
Faculty of Engineering & Architecture