

JAI NARAIN VYAS UNIVERSITY, JODHPUR
FACULTY OF ENGINEERING

NO.: JNVU/FE/PP:EXAM/CSE/2016/SP-8

DATE:08/09/2016

NOTICE

The Practical & sessional examination of the BE II year IV Semester examination 2016 (**Electronics and Computer Engineering**) will be held as per the programme given below. All concerned please note.

S.NO.	Subject	Roll No.			
		15ECC30001- 15ECC30015	15ECC30016- 15ECC30030	15ECC30031- 15ECC30045	15ECC30046- Rest
1.	Electronics Circuits Lab	16/9 (M)	16/9 (A)	17/9(M)	17/9 (M)
2.	Digital Electronics Lab	17/9 (M)	17/9 (A)	16/9(M)	16/9 (M)
3.	Logic Design Simulation and Testing	19/9 (M)	19/9 (A)	20/9(M)	20/9 (M)
4.	Electronics Workshop	21/9 (M)	21/9 (A)	22/9 (M)	22/9 (A)
5.	Programing Languages Lab	22/9 (M)	22/9 (A)	21/9 (M)	21/9 (A)

(M): Morning session starts from 8:00 a.m. onwards.

(A): Afternoon session starts from 11:00 a.m. onwards.



(Dr. Anil Gupta)

Copy to following for information and necessary action please:

1. The Dean, Faculty of Engineering.
2. The Head of dept. **Electronics**
3. Examiner concerned.
4. The Asstt. Registrar, Secrecy / Examination / Engineering
5. P.S. to Vice Chancellor / Registrar, J.N.V. University, Jodhpur



(Dr. Anil Gupta)

Coordinator (Examination)

Faculty of Engineering & Architecture